

[FIG.1]

POWER VALUE

1 CHIP

TIME

5

[FIG.2]

102 RECEPTION SECTION

103 DELAY PROFILE CREATION SECTION

104 PATH DETECTION SECTION

10 105 PATH LIMITATION SECTION

106 DEMODULATION SECTION

RECEIVED DATA

[FIG.3]

15 FROM DELAY PROFILE CREATION SECTION 103

201 ADJACENT SAMPLE COMPARISON PROCESSING SECTION

202 DELAY PROCESSING SECTION

203 PEAK DETECTION PROCESSING SECTION

204 COMPARISON RESULT STORAGE PROCESSING SECTION

20 205 SLOPE DECISION PROCESSING SECTION

206 MAXIMUM VALUE DETECTION PROCESSING SECTION

207 PATH SELECTION SECTION

TO PATH LIMITATION SECTION 105

TO PATH LIMITATION SECTION 105

25

[FIG.4]

START

ST301 INITIALIZATION

ST302 (FALLING?)
ST303 (IS IMMEDIATELY PRECEDING SAMPLE RISING?)
ST305 ADD DP[i-1] TO PATH CANDIDATE
ST306 MAXIMUM VALUE?
5 ST307 REGISTER AS MAXIMUM VALUE
ST308 MOVE POINTER
ST309 ALL DATA COMPLETED?
ST310 (FALLING FOUR TIMES?)
ST311 ADD DP[i] TO PATH CANDIDATE
10 ST316 (RISING FOUR TIMES?)
ST317 ADD DP[i] TO PATH CANDIDATE
END

[FIG.5]

15 POWER VALUE
PART TO BE DETECTED
RISE
FALL
PATH
20 TIME

[FIG.6]

POWER VALUE
RISE
25 FALL #1
FALL #2
FALL #3
FALL #4

PART TO BE DETECTED

PATH

TIME

5 [FIG.7]

POWER VALUE

PART TO BE DETECTED

RISE

FALL

10 PATH

PATH

TIME

[FIG.8]

15 COMPARISON RESULT

SHAPE OF DELAY PROFILE FROM 3 SAMPLES AHEAD TO CURRENT
ONE

"YES" → "YES" RISING

"YES" → "NO" WITH PEAK

20 "NO" → "NO" FALLING

"NO" → "YES" WITH MINIMUM

[FIG.9]

FROM DELAY PROFILE CREATION SECTION 103

25 201 ADJACENT SAMPLE COMPARISON PROCESSING SECTION

202 DELAY PROCESSING SECTION

203 PEAK DETECTION PROCESSING SECTION

204 COMPARISON RESULT STORAGE PROCESSING SECTION

205 SLOPE DECISION PROCESSING SECTION
206 MAXIMUM VALUE DETECTION PROCESSING SECTION
207 PATH SELECTION SECTION
TO PATH LIMITATION SECTION 105
5 TO PATH LIMITATION SECTION 105

[FIG.10]

START
ST901 INITIALIZATION
10 ST902 (FALLING?)
ST903 (IS IMMEDIATELY PRECEDING SAMPLE RISING?)
ST904 (FOUR TIMES RISING?)
ST905 ADD DP[i-5] TO PATH CANDIDATE
ST911 MOVE POINTER
15 ST912 ALL DATA COMPLETED?
END

[FIG.11]

POWER VALUE
20 PART TO BE DETECTED
RISE
FALL
PATH
PATH
25 TIME

[FIG.12]

102 RECEPTION SECTION

103 DELAY PROFILE CREATION SECTION
1101 PATH DETECTION SECTION
105 PATH LIMITATION SECTION
106 DEMODULATION SECTION
5 RECEIVED DATA

[FIG.13]

FROM DELAY PROFILE CREATION SECTION 103
201 ADJACENT SAMPLE COMPARISON PROCESSING SECTION
10 202 DELAY PROCESSING SECTION
203 PEAK DETECTION PROCESSING SECTION
204 COMPARISON RESULT STORAGE PROCESSING SECTION
205 SLOPE DECISION PROCESSING SECTION
207 PATH SELECTION SECTION
15 TO PATH LIMITATION SECTION 105